



Fee only

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. OSADA, et al.
Application No.: 10/686,569
Filed: October 17, 2004
For: SEMICONDUCTOR MEMORY APPARATUS,
SEMICONDUCTOR APPARATUS, DATA PROCESSING
APPARATUS AND COMPUTER SYSTEM
Art Unit: 2818
Examiner: H. V. Ho

TERMINAL DISCLAIMER

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

June 29, 2004

Sir:

Through its undersigned attorneys, Renesas Technology Corp., having a principal place of business at 4-1, Marunouchi 2-Chome, Chiyoda-ku, Tokyo, Japan, represent that as a result of an assignment recorded in the United States Patent and Trademark Office they are the owners of 100% of the interest in the above-identified patent application.

Petitioner hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on said patent application which would extend beyond the full statutory term of U.S. Patent Number 6,515,894. Petitioner hereby agrees that any patent so granted on said patent application shall be enforceable only for and during such period that such patent and the above-listed patent are commonly owned. This agreement runs with any patent granted on the above-identified application and is binding upon the grantee, its successors or assigns.

In making the above disclaimer petitioner does not disclaim any terminal part of any patent granted on the above-identified application that would extend to the